

+

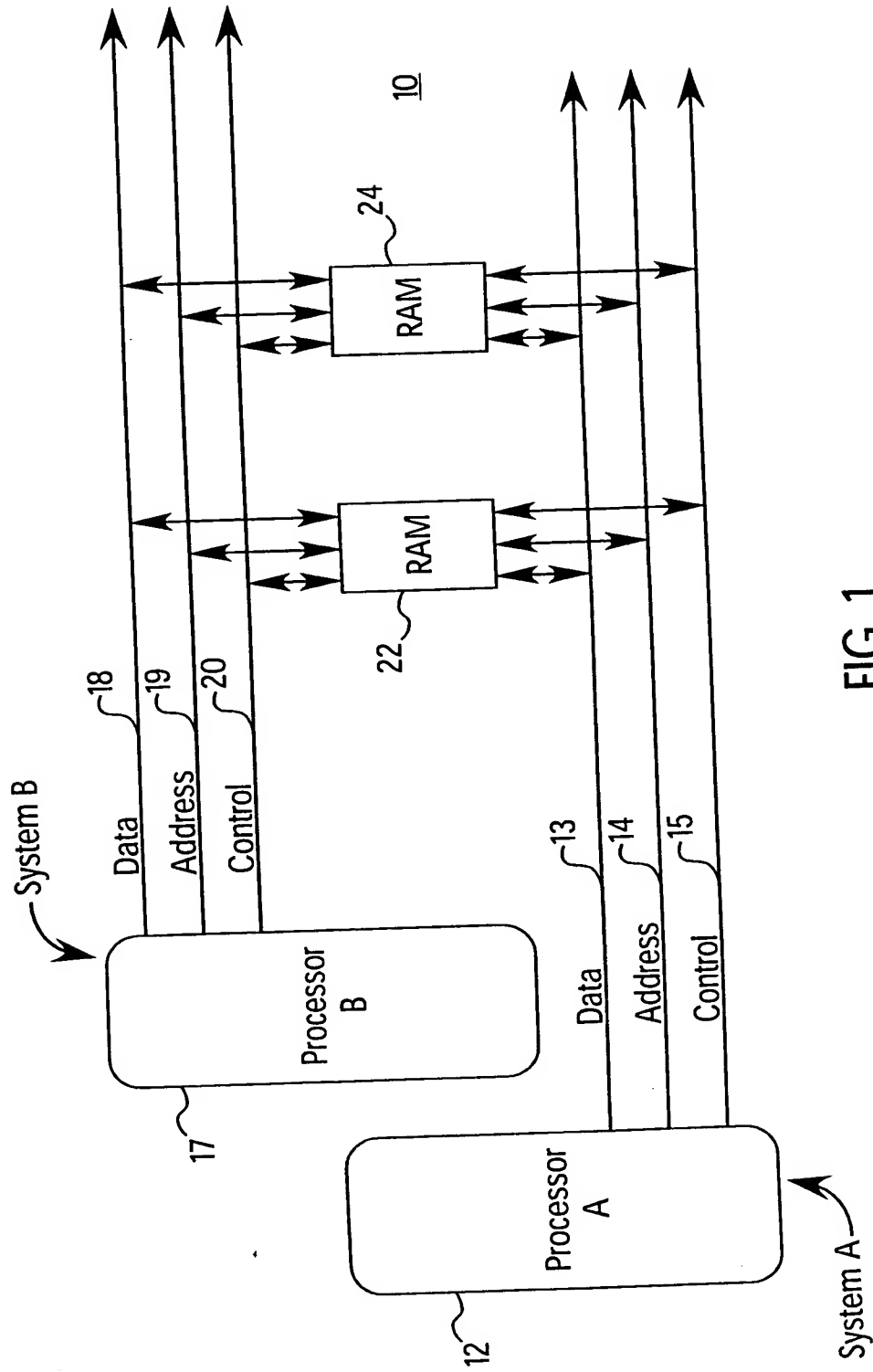
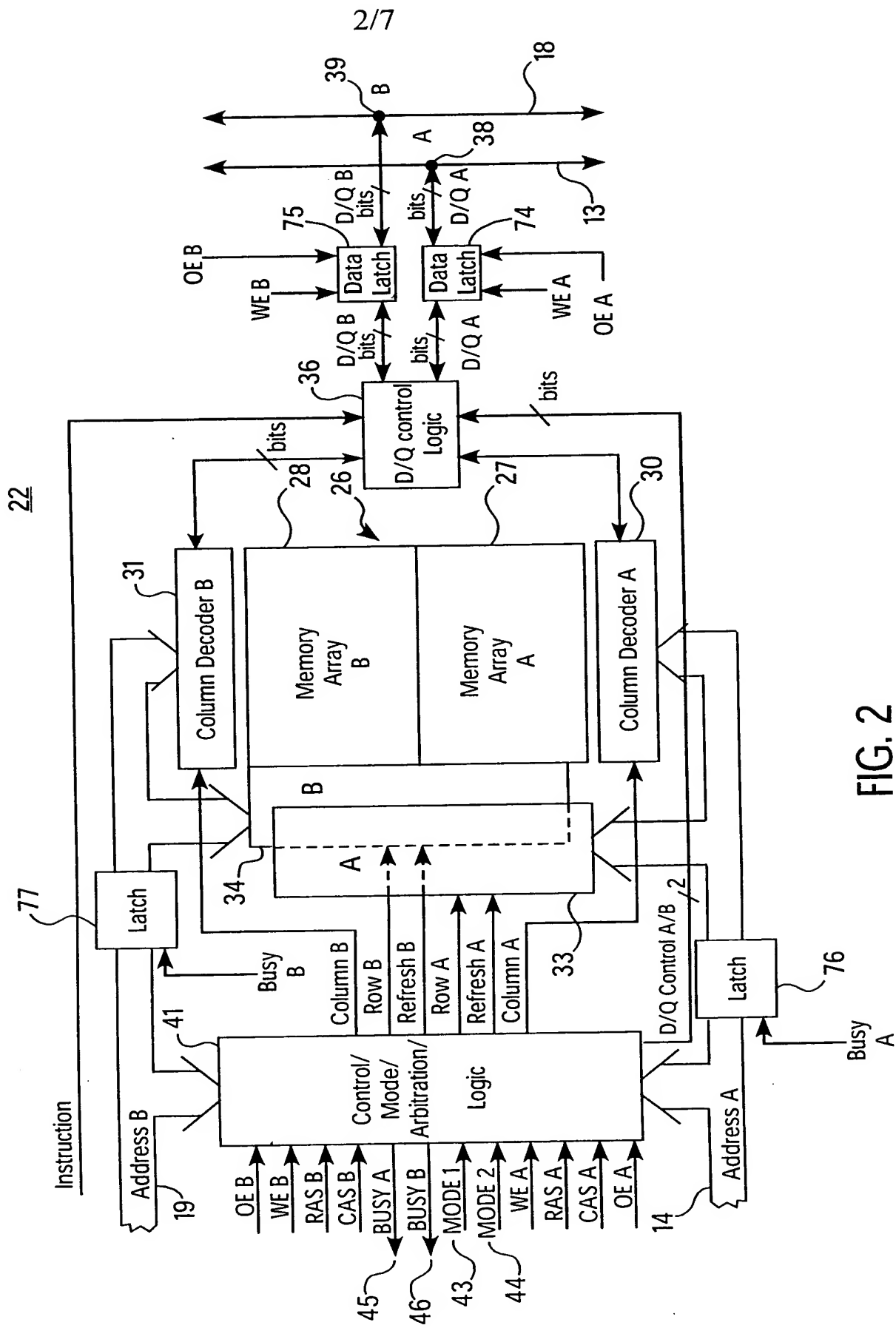


FIG. 1

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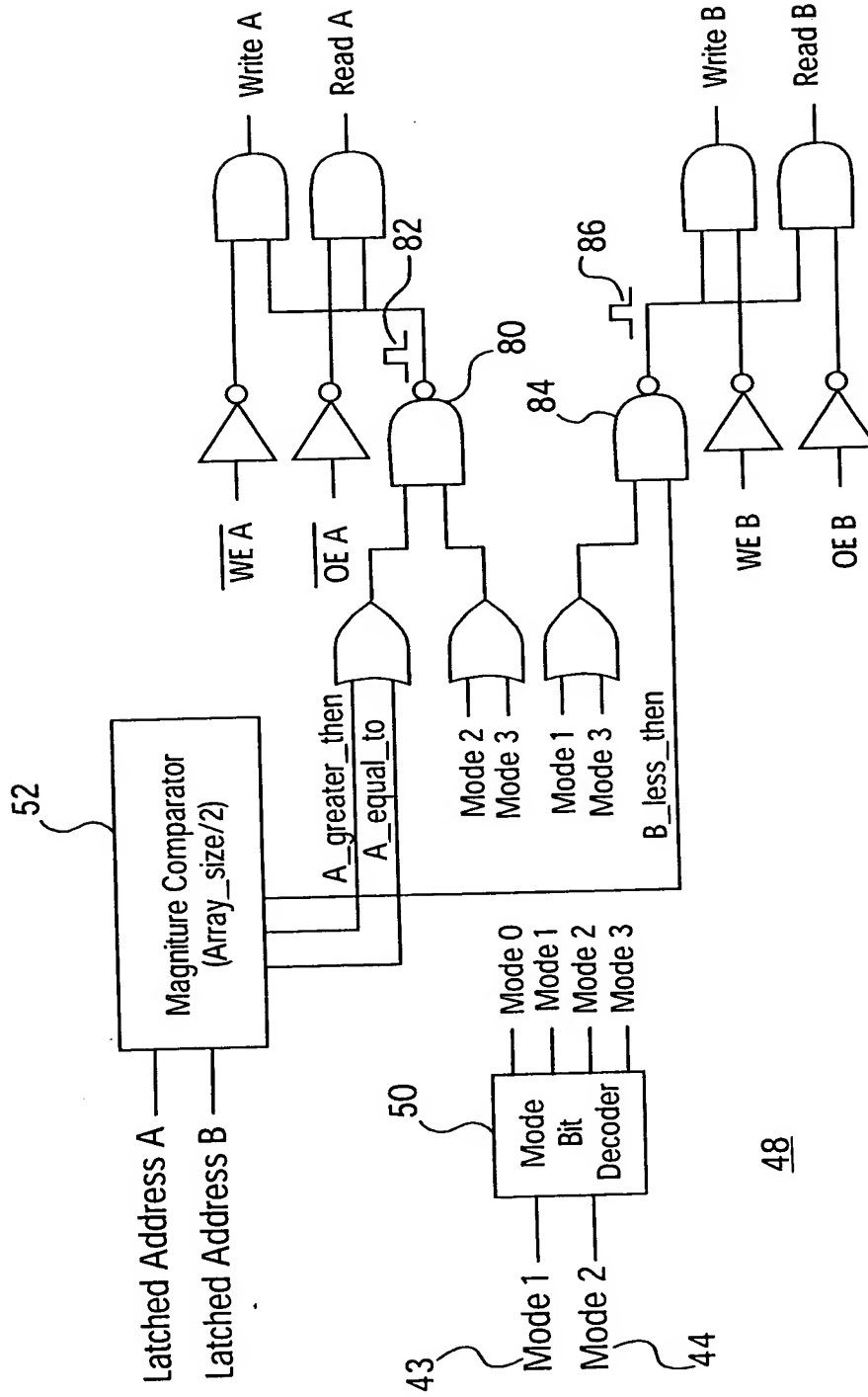


FIG. 3

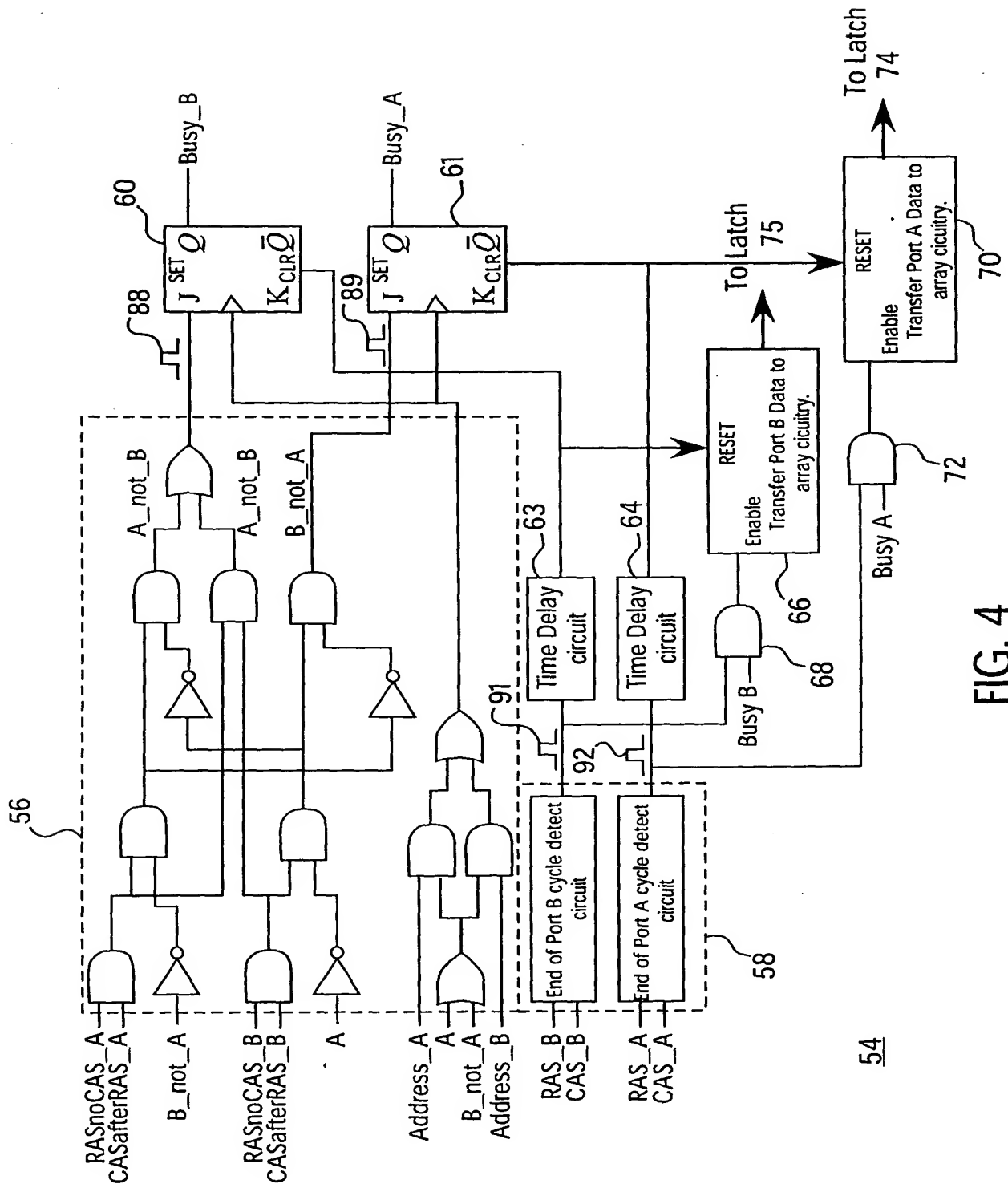


FIG. 4

Simultaneous
Access

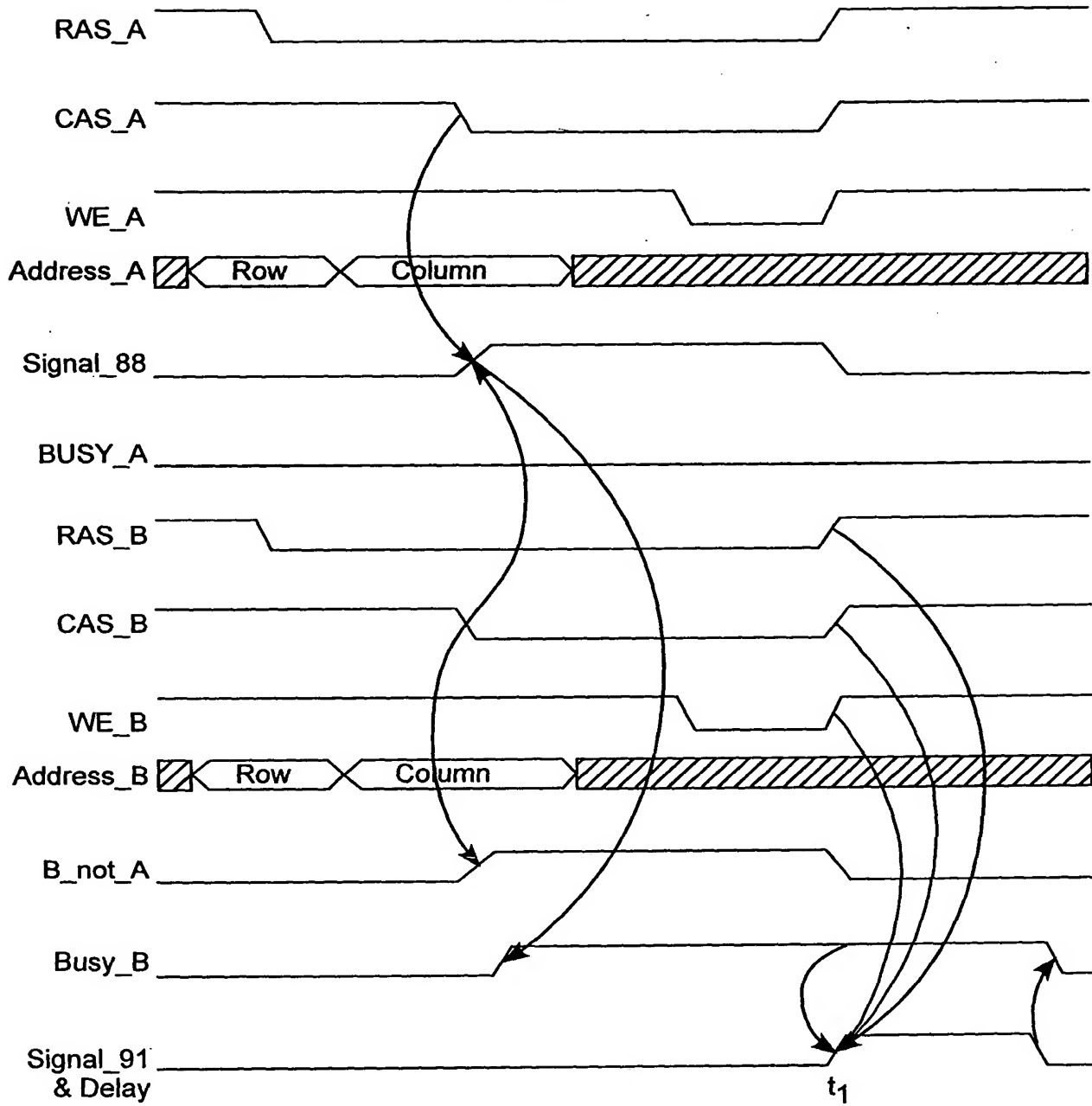


FIG. 5

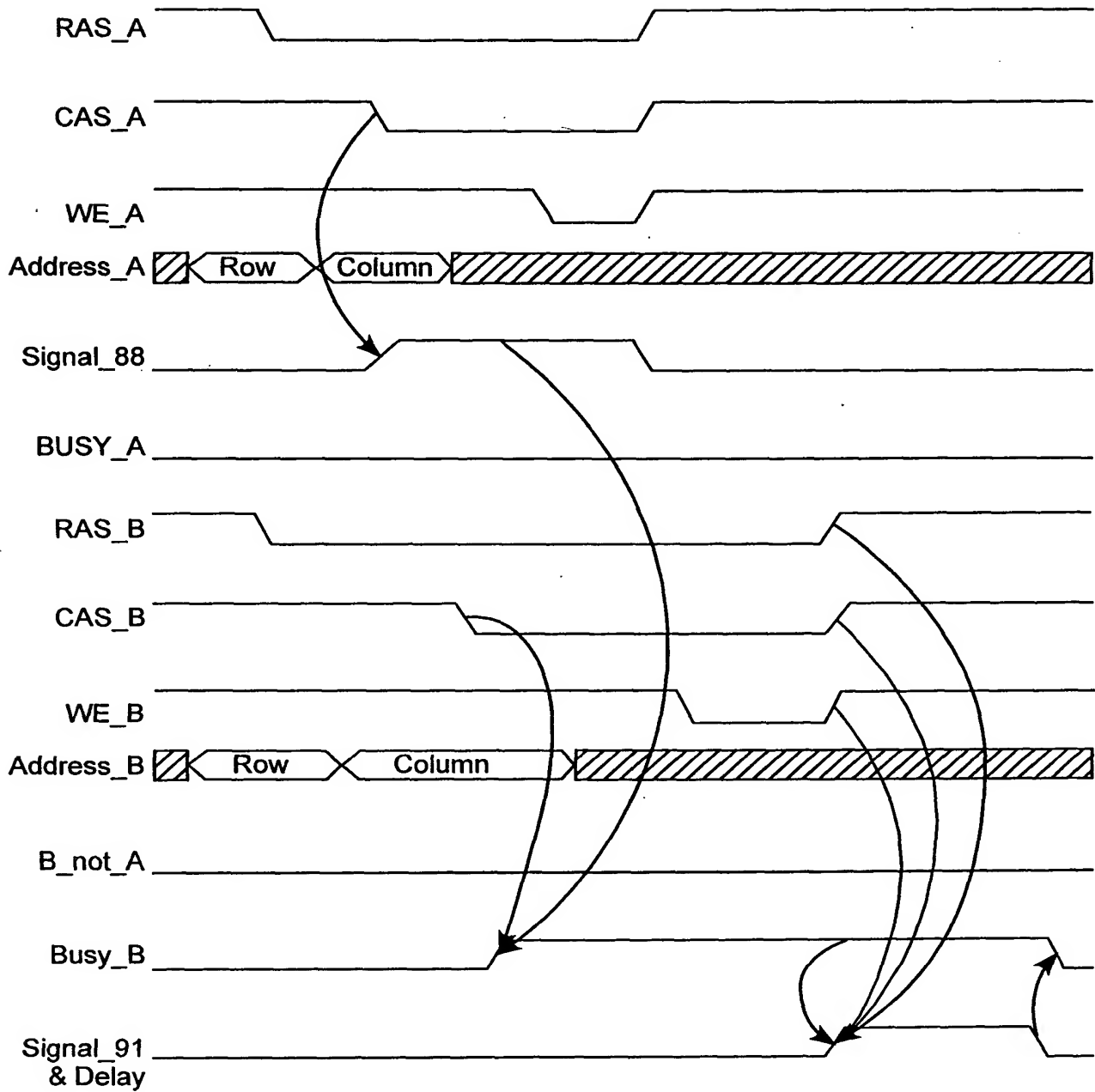
Sequential
Access

FIG. 6

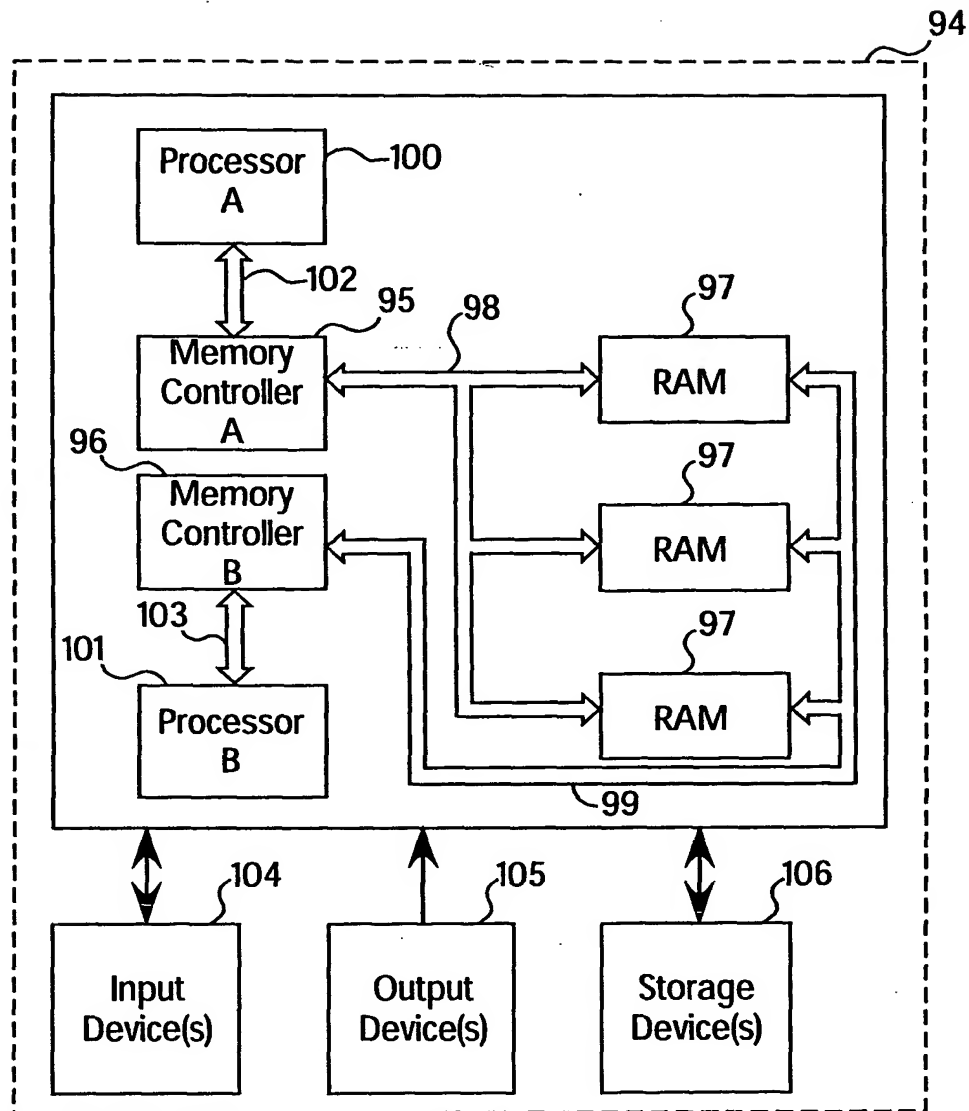


FIG. 7